## IN THE UNITED STATES PATENT & TRADEMARK OFFICE

ATTORNEY DOCKET NUMBER: 10006168-5

IN RE APPLICATION OF: U.S. Patent No. 6,872,964

LISPTO CONFIRMATION CODE: 6592

APPLICATION NO.: 10/644,503

FILED: Aug. 20, 2003

EXAMINER: Nathan J. Flynn

GROUP ART UNIT: 2826

FOR: DATA STORAGE DEVICE

## 37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION

## HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS

Sir:

The following is a request for a certificate of correction in Serial Number 10/644,503, now Patent Number 6,872,964.

A certificate of correction under 35 USC 254 is respectfully requested in the above-identified patent.

| $\boxtimes$ | All errors were the fault of the USPTO, no fee required. In the event that a furthe |
|-------------|---|
| fee         | is required, please charge the amount to Deposit Account No. 082025.                |

|        | All errors were the fault of the applicant and, accordingly, please charge \$100.00 |
|--------|---|
| to our | Deposit Account No. 082025. In the event that a further fee is required, please     |
| charge | the amount to the same Deposit Account.   |

The errors were the fault of both the applicant and USPTO and, accordingly, please charge \$100.00 to our Deposit Account No. 082025. In the event that a further fee is required, please charge the amount to the same Deposit Account.

The exact locations where the errors appear in the patent and patent application are as follows:

On the face page, in field (56), under "FOREIGN PATENT DOCUMENTS", in column 2, line 4, delete "EP" and insert - - WO - -, therefor. List of References cited by applicant and considered by examiner dated Dec. 20, 2004, sheet 1 of 1, under "FOREIGN PATENT DOCUMENTS", entry 4)

In column 5, line 2, delete "10" and insert - - 110 - -, therefor. (Specification filed on Aug. 20, 2003, page 9, line 23)

In column 5, line 15, delete "a "l" bit," and insert - - a "1" bit, - -, therefor. (Specification filed on Aug. 20, 2003, page 10, line 10)

In column 5, line 51, delete "FIG. 1" and insert - - FIG. 5 - -, therefor. (Specification filed on Aug. 20, 2003, page 11, line 13)

In column 9, line 57, after "understood" insert - - by those skilled in - -. (Specification filed on Aug. 20, 2003, page 20, line 1)

In column 9, line 58, after "without" insert - - departing from the - -. (Specification filed on Aug. 20, 2003, page 20, line 2)

In column 10, line 37, in Claim 14, after "device" insert --, --. (Amendment to claims filed on Sep. 02, 2004, page 4, in Claim 49, line 2)

In column 10, line 52, in Claim 17, delete "(MM)" and insert - - (MIM) - -, therefor.

(Amendment to claims filed on Sep. 02, 2004, page 5, in Claim 52, line 3)

In column 11, line 15, in Claim 24, delete "n++semiconductor" and insert - - n++ semiconductor - -, therefor. (Amendment to claims filed on Sep. 02, 2004, page 6, in Claim 59, line 2)

The requested corrections are attached on Form PTO 1050.

Respectfully Submitted

Name: Douglas M. Gilbert Registration No.: 27,196

Attorney/Agent of Record

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1/17/2009

## UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 6,872,964

APPLICATION NO.: 10/644,503

ISSUE DATE : Mar. 29, 2005

INVENTOR(S) : Steven L. Naberhuis, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "FOREIGN PATENT DOCUMENTS", in column 2, line 4, delete "EP" and insert - - WO - -, therefor.

In column 5, line 2, delete "10" and insert - - 110 - -, therefor.

In column 5, line 15, delete "a "I" bit," and insert - - a "1" bit, - -, therefor.

In column 5, line 51, delete "FIG. 1" and insert - - FIG. 5 - -, therefor.

In column 9, line 57, after "understood" insert - - by those skilled in - -.

In column 9, line 58, after "without" insert - - departing from the - -.

In column 10, line 37, in Claim 14, after "device" insert - - , - -.

In column 10, line 52, in Claim 17, delete "(MM)" and insert -- (MIM) --, therefor.

In column 11, line 15, in Claim 24, delete "n++semiconductor" and insert - - n++ semiconductor - -, therefor.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

HEWLETT-PACKARD COMPANY Intellectual Property Administration P. O. Box 272400 Fort Collins, Colorado 80527-2400

This collection of information is required by 37 CPR, 1322, and 1323, and 1334. The information is required to obtain or retain a benefit by the public which is to life (and by the URFOT to process) as population. Confidentially is governed by \$150. CS 22 and \$7 CFR. 11. This collection is entimated to take 10 how to complete, inclining gathering, preparing, and submitting the completed application form to the USFTO. Time will way deposing upon the individual teast. Any comments on the amount of time you require to complete this form and/or suggestion for forminging the based, as body does set to the CHEF information CHEFF. 12 Pearse and Trademath Office, U.S. Department of Commerce, P.O. Box 1450, Alexandra, VA, 22313-1450, DO NOT STON PEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Pearles, P.O. Box 1454, ARRANGRA, VA, 22313-1450, DO NOT STON PEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Pearles, P.O. Box 1454, ARRANGRA, VA, 22315-1450, DO NOT STON PEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Pearles, P.O. Box 1454, ARRANGRA, VA, 22315-1450.